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April 4, 2002

BOX PCT

Commissioner for Patents
Washington, D.C. 20231

PCT/EP00/10104
-filed October 2, 2000

Re: Application of Antonio FONTAN TARODO, Jorge GONZALEZ GONZALEZ,
Antonio J. HUERTAS BLAZQUEZ
SWITCHED POWER SUPPLY CONVERTER FOR BROAD RANGE OF
INPUT VOLTAGES
Assignee: ALCATEL
Our Ref: Q69184

Dear Sir:

The following documents and fees are submitted herewith in connection with the above application for the purpose of entering the National stage under 35 U.S.C. § 371 and in accordance with Chapter II of the Patent Cooperation Treaty:

- an executed Declaration and Power of Attorney.
- an English translation of the International Application.
- 1 sheet of drawing.
- an English translation of Article 19 claim amendments.
- an English translation of Article 34 amendments (annexes to the IPER).
- an executed Assignment and PTO 1595 form.
- a PTO/SB/08 A & B (modified) (substitute for PTO Form 1449) listing the ISR references, and a complete copy of each reference.
- a Preliminary Amendment

The Declaration and Power of Attorney, Assignment, Preliminary Amendment will be submitted at a later date.

It is assumed that copies of the International Application, the International Search Report, the International Preliminary Examination Report, and any Articles 19 and 34 amendments as required by § 371(c) will be supplied directly by the International Bureau, but if further copies are needed, the undersigned can easily provide them upon request.



SUGHRUE MION, PLLC

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Washington, D.C. 20231
Attorney Docket Q69184
Page 2
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**PLEASE NOTE THAT THE FILING FEE IS BASED ON THE ARTICLE 34
AMENDMENTS TO THE CLAIMS AND PRELIMINARY AMENDMENT ATTACHED
HERETO**

The Government filing fee is calculated as follows:

Total claims	<u>7</u>	-	<u>20</u>	=	_____	x	\$18.00	=	_____	\$0.00	
Independent claims	<u>1</u>	-	<u>3</u>	=	_____	x	\$84.00	=	_____	\$0.00	
Base Fee											<u>\$890.00</u>
TOTAL FEE											<u>\$890.00</u>

A check for the statutory filing fee of \$890.00 is attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16, 1.17 and 1.492 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

Priority is claimed from:

<u>Country</u>	<u>Application No</u>	<u>Filing Date</u>
Spain	P9902189	October 5, 1999

Respectfully submitted,



David J. Cushing
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Date: April 4, 2002

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JC10 Rec'd PCT/PTO 04 APR 2002
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of PCT/EP00/10104
Antonio FONTAN TARODO, et al. Attorney Docket Q69184
Appln. No.: Not Assigned Group Art Unit: Not Assigned
Confirmation No.: Not Assigned Examiner: Not Assigned
Filed: April 04, 2002
For: SWITCHED POWER SUPPLY CONVERTER FOR BROAD RANGE OF INPUT
VOLTAGES

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please enter the following amended claim:

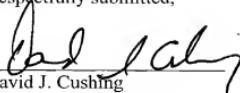
7. (Amended) Switched power supply converter according to claim 1, characterised
in that the first control circuit and the second control circuit are independent.

Preliminary Amendment
Attorney Docket Q69184

REMARKS

Entry and consideration of this Amendment are respectfully requested.

Respectfully submitted,


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Date: April 4, 2002

Preliminary Amendment
Attorney Docket Q69184

APPENDIX

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS:

The following claim amended as follows:

7. (Amended)Switched power supply converter according to any of claims 1 to 6, characterised in that the first control circuit and the second control circuit are independent.

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SWITCHED POWER SUPPLY CONVERTER FOR BROAD RANGE OF INPUT VOLTAGES**OBJECT OF THE INVENTION**

The present invention relates to a switched power supply converter that comprises at least one switching element with which the transfer of energy between the input and the output of the power supply converter is governed.

The switching element is controlled so that its duty cycle is at all times a function of the value of the output voltage, whereby the power supply converter offers a high efficiency over a universal range of input voltages.

The switched power supply converter is of special, but not exclusive application in telecommunications systems, which are fed from voltage sources of 38 to 380 V.

STATE OF THE ART

A switched power supply converter which has a switching element whose duty cycle is variable and which receives a broad range of input voltages has been described, for example, in the US Patent 5,856,739 granted to A. Trica, incorporated in the present patent application by reference.

The switched converter, implemented according to a buck topology, comprises a switching element that has a high switching frequency and a variable duty cycle, an internal control current loop, an external control voltage loop and a control circuit that controls the duty cycle of the switching element as a function of the current loop and of the voltage loop.

The power supply converter accepts a broad range of input voltages of up to four times the output voltage. The converter is capable of working in voltage ranges that include voltage values supplied from batteries and from alternating current supply sources. However it is incapable of working with higher ranges, for example 10:1, and providing power levels equal to or greater than 100 W.

In the US Pat 5,006,782 granted to Pelly, teaches a two or more buck converter circuits are cascaded in such a manner that the output of one serves as the input to the next, with the input voltage to each succeeding buck converter stage being reduced in magnitude.

The first stage of the buck converter containing a first switching transistor having an adjustable duty cycle to produce a nominally fixed output voltage. The output voltage of the first stage is lower than the minimum input

1-a

voltage but is higher than the desired final output voltage of the cascaded buck converter. The output voltage of the first stage forms the input voltage of the second stage. The switching transistor in the second stage has a nominally fixed duty cycle sufficient to reduce its input voltage, which corresponds to the output voltage of the first stage, to some fixed output voltage for the second stage.

5

The first stage is designed to reduce input voltage of 450 volts to some constant value 80 volts. This is accomplished by continually adjusting the duty cycle of the switching transistor in the first stage.

10 Unfortunately, when the input voltage is outside of the range between 450 volts and 80 volts, first stage does not teaches how is adjusting the duty cycle of the switching transistor in the first stage.

15 It has become necessary to develop a switched power supply converter that accepts a universal range of input voltages, which includes the voltage values supplied normally by the batteries of telecommunications systems, and guarantees for all of them the provision of a constant and regulated voltage at its output, so that the converter offers a high efficiency over the entire range of input voltages.

CHARACTERISATION OF THE INVENTION

20 To overcome the problems outlined above a switched power supply converter for a broad range of input voltages is proposed which is of ideal dimensions and electrical operating characteristics for supplying telecommunications systems with electrical power equal to or greater than 100 W.

25 An object of the switched power supply converter of the invention is to provide a converter that works with a very broad voltage range, for example 38 to 380 V (10:1), with simple overall operation and high overall performance. The power supply converter is implemented by means of two conversion stages connected in cascade. Both stages are implemented by means of straightforward, highly efficient conversion topologies.

A further object is that both conversion stages have a control circuit for regulating respectively their output voltage, the regulation processes being independent of each other.

30 The control circuit for the first stage regulates the duty cycle of a switching element of the first stage in the event that the input voltage lies within

1-h

a predetermined range of input voltages, and when the input voltage is outside said range, the duty cycle is set to a value so that the output voltage of the first stage is proportional to the input voltage. As a consequence, the first stage of the converter has a constant output voltage over a wide range of input voltages.

5 voltages of the first stage. Then, it is possible to optimise the operation of the components of the second stage, in particular for boosting its efficiency.

The switched power supply converter for broad range of input voltages of the invention is divided into a first stage that converts a first voltage supplied from a voltage source into a second voltage by means of a first switching element; a second stage receives the second voltage and transforms it into a third DC voltage.

A control circuit controls the duty cycle of the first switching element so that the duty cycle varies between a first limit of the duty cycle and a second limit of the duty cycle when the first voltage is within a predetermined range of voltage values. The control circuit sets the duty cycle to the first limit of the duty cycle or to the second limit of the duty cycle in the event that the first voltage lies outside the predetermined range of voltage values.

over the entire range of input voltages.

CHARACTERISATION OF THE INVENTION

To overcome the problems outlined above a switched power supply converter for a broad range of input voltages is proposed which is of ideal 5 dimensions and electrical operating characteristics for supplying telecommunications systems with electrical power equal to or greater than 100 W.

An object of the switched power supply converter of the invention is to provide a converter that works with a very broad voltage range, for example 10 38 to 380 V (10:1), with simple overall operation and high overall performance. The power supply converter is implemented by means of two conversion stages connected in cascade. Both stages are implemented by means of straightforward, highly efficient conversion topologies.

A further object is that both conversion stages have a control circuit 15 for regulating respectively their output voltage, the regulation processes being independent of each other.

The control circuit for the first stage regulates the duty cycle of a switching element of the first stage in the event that the input voltage lies within a predetermined range of input voltages, and when the input voltage 20 is outside said range, the duty cycle is set to a value so that the output voltage of the first stage is proportional to the input voltage. As a consequence, the range of input voltages of the second stage is less than the range of input voltages of the first stage. Then, it is possible to optimise the operation of the components of the second stage, in particular for boosting its efficiency.

The switched power supply converter for broad range of input voltages of the invention is divided into a first stage that converts a first voltage supplied from a voltage source into a second voltage by means of a first switching element; a second stage receives the second voltage and 30 transforms it into a third DC voltage.

A control circuit controls the duty cycle of the first switching element 35 so that the duty cycle varies between a first limit of the duty cycle and a second limit of the duty cycle when the first voltage is within a predetermined range of voltage values. The control circuit sets the duty cycle to the first limit of the duty cycle or to the second limit of the duty cycle in the event that

the first voltage lies outside the predetermined range of voltage values.

BRIEF DESCRIPTION OF THE FIGURES

A more detailed explanation of the invention is given in the following description, based on the attached figures, in which:

5 - figure 1 shows a block diagram of a preferred embodiment of a switched power supply converter according to the invention.

DESCRIPTION OF THE INVENTION

Figure 1 shows a block diagram of a preferred embodiment of a switched power supply converter for broad range of input voltages. The power supply converter has a first stage 11 and a second stage 21 connected in cascade.

10 The first stage 11 of the power supply converter is connected to a power supply source via some input terminals 11-1 and 11-2, which correspond to the input terminals of the power supply converter. For example, the terminal 11-1 is connected to the positive pole and the terminal 11-2 to ground, respectively.

15 The first stage 11 is adapted to convert a broad range of input voltage values, first input voltage, into a predetermined range of output voltage, second output voltage, across some output terminals 12-1 and 12-2, which correspond to some input terminals of the second stage 21. Thus, this second voltage is directly fed to the input of the second stage 21.

20 The values that are possible for the second output voltage of the first stage 11 to adopt are such that they permit the stress level to be low in some switching elements included in the second stage 21, and also prevents them from having to support a high current spike.

25 It is possible to select different conversion topologies both for the first stage 11 and for the second stage 21, all of said topologies being known in the state of the art.

30 In a first embodiment for the first stage 11 a conversion topology without galvanic isolation is chosen, which is highly efficient and of straightforward operation; and for the second stage 21 a conversion topology is chosen that has a transformer T. In this manner, the second stage 21 provides galvanic isolation between the input and output of the switched power supply converter, permits the power supply converter to be designed with various outputs, as well as compliance with safety standards.

The first stage 11 comprises at least a first switching element 11-3, such as a field-effect transistor MOSFET, in order to perform the chopping of the first voltage applied across the input terminals 11-1 and 11-2; and produces across its output terminals 12-1 and 12-2 the second voltage, 5 through the control of the duty cycle of the first switching element 11-3.

The regulation process of the second voltage is achieved by varying the duty cycle of the first switching element 11-3 by means of a control circuit 11-9, for example a pulse width modulation device, which includes a control logic to carry out missions such as regulation of the second voltage, 10 limitation of the duty cycle of the first switching element 11-3, and others.

It is possible for the duty cycle to be limited to a maximum duty cycle (first limit of the duty cycle) or to a minimum duty cycle (second limit of the duty cycle).

The first stage 11 regulates the second voltage by means of the 15 control circuit 11-9 in the event that the value of the first input voltage applied across its terminals 11-1 and 11-2 is within a predetermined voltage range, i.e., the control circuit 11-9 produces a duty cycle which is within a predetermined range of the duty cycle, which is defined by means of the first limit and the second limit of the duty cycle, so that the second voltage 20 applied across the output terminals 12-1 and 12-2 is stabilised.

However, when the value of the input voltage applied across the terminals 11-1 and 11-2 is above or below the predetermined voltage range, the control circuit 11-9 generates a constant duty cycle, the value of which coincides with one of the limits of the predetermined range of the duty cycle, 25 i.e., the first stage 11 does not regulate its output voltage, merely generates the voltage corresponding to one of the limits of the duty cycle (maximum duty cycle or minimum duty cycle). The control circuit 11-9 sets the limit of the duty cycle by means of its control logic.

In brief, the first stage 11 regulates the second output voltage for a 30 range of the first input voltage and for values of input voltage outside this voltage range the first stage 11 produces across its output terminals 12-1 and 12-2 a second voltage proportional to the first input voltage.

In both situations, the second voltage present across the terminals 12-1 and 12-2 is such that it permits the stress level to be low in the 35 switching elements of the second stage 21, and also prevents them from

having to support a high current spike.

The embodiment of the first stage 11 is possible by means of different conversion topologies without galvanic isolation such as a buck converter or a boost converter. The converters without galvanic isolation are 5 implemented with a minimum of components implying that it is a converter free of operational complexity. In both topologies the transfer of energy is performed inductively since it can be considered that the connection between the input and the output is achieved via an inductor through the first switching element 11-3.

10 The second stage 21 is adapted to transform the second voltage into a third voltage by the action of a transformer T. Then, it is possible to implement said stage 21 according to different conversion topologies with galvanic isolation such as a forward converter with active clamp or a flyback converter. Both converters have the property of including galvanic isolation, 15 however the latter is mounted in a different position. The galvanic isolation is provided by means of the transformer T.

Therefore, the second stage 21 provides galvanic isolation between the input and the output of the switched power supply converter; additionally, with a simple change of turns ratio a change between a step-down and a 20 step-up output is facilitated and it is also possible to provide various outputs for the switched power supply converter.

It is also possible to achieve a change of polarity in the output voltage by merely changing the wiring of transformer T. Before the transformed voltage reaches the load, it has to be filtered to produce the third stabilised 25 voltage, which shall correspond to the output of the converter. The second stage 21 performs the regulation of the third voltage by means of a second control circuit that extracts a sample of the third voltage.

The topologies mentioned above are known in the state of the art, consequently their operation is not explained herein. The first stage 11 and 30 the second stage 21 of the switched power supply converter can be implemented according to other conversion topologies.

The switched power supply converter of the invention has a high overall efficiency and its operation is straightforward, in spite of having two conversion stages 11 and 21, with their corresponding control loops, which 35 are independent.

- 6 -

By means of a rectifier bridge, the input terminals 11-1 and 11-2 of the switched power supply converter are connected to an AC voltage source.

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CLAIMS

1. Switched power supply converter for broad range of input voltages that comprises a first stage (11) which converts a first voltage supplied from a voltage source into a second voltage by means of a first switching element (11-3) and a second stage (21) that receives the second voltage and transforms it into a third DC voltage, a first control circuit (11-9) controls the duty cycle of the first switching element (11-3) so that the duty cycle varies between a first limit of the duty cycle and a second limit of the duty cycle; **characterised** in that the first control circuit (11-9) is adapted to fix the duty cycle at the first limit of the duty cycle or at the second limit of the duty cycle in the event that the first voltage is outside a predetermined range of voltage values.

2. Switched power supply converter according to claim 1, **characterised** in that the first control circuit (11-9) is adapted to receive a sample of the second voltage.

3. Switched power supply converter according to claim 1, **characterised** in that the first stage (11) is implemented according to a conversion topology without galvanic isolation.

4. Switched power supply converter according to claim 1, **characterised** in that the second stage (21) is implemented according to a conversion topology with galvanic isolation.

5. Switched power supply converter according to claim 5, **characterised** in that the second stage (21) comprises a transformer (T) with a predetermined number of secondary windings that configure a predetermined number of outputs of the switched power supply converter, respectively.

6. Switched power supply converter according to claim 6, **characterised** in that the second stage (21) comprises a second control circuit that is adapted to receive a sample of the third voltage and regulates the third voltage.

7. Switched power supply converter according to any of claims 1 and 6, **characterised** in that the first control circuit and the second control circuit are independent.

SWITCHED POWER SUPPLY CONVERTER FOR BROAD RANGE OF INPUT VOLTAGES

Abstract

Switched power supply converter for broad range of input voltages that comprises a first stage (11) connected in cascade with a second stage (21), such that a first voltage supplied from a voltage source is converted into a second voltage by means of a first switching element (11-3). The second voltage is transformed into a third DC voltage by the operation of the second stage (21).

A control circuit (11-9) controls the duty cycle of the first switching element (11-3) so that the duty cycle varies between a first limit and a second limit of the duty cycle when the first voltage lies within a predetermined voltage range. The control circuit (11-9) sets the duty cycle to the first limit of the duty cycle or to the second limit of the duty cycle in the event of the first voltage being outside the predetermined range of voltage values.

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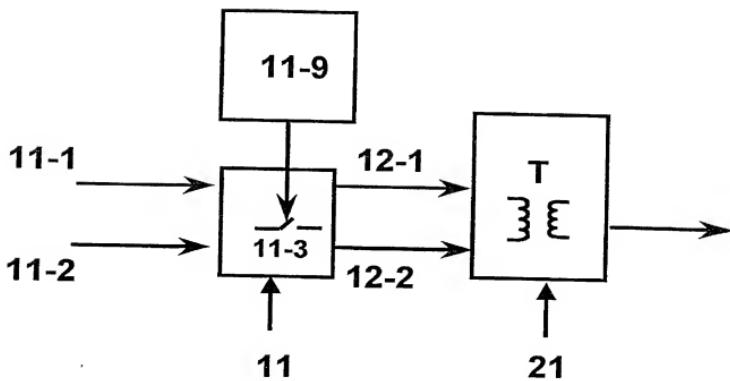


FIG. 1

DECLARATION AND POWER OF ATTORNEY FOR UTILITY OR DESIGN PATENT APPLICATION (37 CFR 1.63)*(Entry into national phase in USA of the PCT International Application n° PCT/EP00/10104 filed on October 2, 2000)*

As a below named inventor, I hereby declare that: My residence, mailing address, and citizenship are as stated below next to my name. I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention in the specification identified by Docket N°

129185/HAS/CNV

I hereby state that I have reviewed and understand the contents of the above identified application, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56, including for continuation-in-part application(s), material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application(s) which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or any PCT international application(s) having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date	Priority Claimed Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>
P 99 02 189	SPAIN	OCTOBER 05 1999	

I hereby claim domestic priority benefits under 35 United States Code §120 of any United States application(s), §119(e) of any United States provisional application(s), or §365(c) of any PCT International application(s) designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in a listed prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge my duty to disclose any information material to the patentability of this application as defined in 37 C.F.R. 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. or International Application Number(s)	U.S. or International Filing Date	Status

I hereby appoint all attorneys of **SUGHRUE MION, PLLC** who are listed under the USPTO Customer Number shown below as my attorneys to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Sughrue Mion, PLLC, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

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PATENT TRADEMARK OFFICE

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Inventor's Signature		Date	
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City	State	Zip	Country

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Given Name (first and middle [if any])	Family Name or Surname		

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Application deficiencies found during scanning:

Page(s) 8 of specification were not present
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